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L28 L24 and I9

30 L28

L27 L24 and I8

10 L27

L26 L24 and I7

55 L26

<u>L25</u>	L24 and I6	73	<u>L25</u>
<u>L24</u>	address\$5 near5 (translat\$6 or convert\$4 or conversion\$1) and I19	80	<u>L24</u>
<u>L23</u>	L19 and I9	43	<u>L23</u>
<u>L22</u>	L19 and I8	14	<u>L22</u>
<u>L21</u>	L19 and I7	114	<u>L21</u>
<u>L20</u>	L19 and I6	140	<u>L20</u>
<u>L19</u>	L18 and address\$4 near5 (tbt or tlb or table)	157	<u>L19</u>
<u>L18</u>	taken and I2	260	<u>L18</u>

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<u>L17</u>	I4 and I9	76	<u>L17</u>
<u>L16</u>	I4 and I8	11	<u>L16</u>
<u>L15</u>	I4 and I7	84	<u>L15</u>
<u>L14</u>	I4 and I6	139	<u>L14</u>
<u>L13</u>	I3 and I9	99	<u>L13</u>
<u>L12</u>	I3 and I8	16	<u>L12</u>
<u>L11</u>	I3 and I7	174	<u>L11</u>
<u>L10</u>	I3 and I6	246	<u>L10</u>
<u>L9</u>	(712/201-219)![CCLS]	4716	<u>L9</u>
<u>L8</u>	(711/201-221)[CCLS]	7018	<u>L8</u>
<u>L7</u>	(712/225-242)[CCLS]	4791	<u>L7</u>
<u>L6</u>	(712/2-300)[CCLS]	13900	<u>L6</u>

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<u>L5</u>	L4 and descriptor\$1	26	<u>L5</u>
<u>L4</u>	L3 and address\$5 near5 (translat\$6 or convert\$4 or conversion\$1)	148	<u>L4</u>
<u>L3</u>	L2 and (prefetch\$5 or fetch\$5)	265	<u>L3</u>
<u>L2</u>	L1 and (tbt or tlb or table) near10 (way\$1 or direct\$4 or path\$1 or target\$3)	265	<u>L2</u>
<u>L1</u>	(branch\$4) near10 (id\$1 or identif\$7) and branch\$3 near1 target near5 predict\$5	489	<u>L1</u>

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